



Honce the ROC of the given discrete sequence is

Hence alternative (a) is the correct choice.

0. (d) The given magnitude plot



The above figure represents lead-lag compensator

9. (b) The given response



We know that

$$V(f) = 2\pi \int |H(f)|^2 \times (f) df$$
  
=  $2\pi \int H(f)|^2 - 1 \times 10^{-10} df$   
=  $1 \times 10^{-10} \times 2\pi \int_0^{10^6/2\pi} 1 df$   
=  $2\pi \times 10^{-10} \times \frac{10^6}{2\pi} = 10^{-6} W$ 

10. (a) The given metallic waveguides



coaxial waveguide has no cut-off frequency

Probability of getting head in only first 2 tosses

$$= {}^{16}C_2 \left(\frac{1}{2}\right)^2 \left(\frac{1}{2}\right)$$
$$= {}^{16}C_2 \left(\frac{1}{2}\right)^{10}$$

Option (d) is correct.

 (b) If the power spectral density of stationary random process is a site squared function of frequency then the shope of its sub correlation will be triangular as shown in option (b).

Hence alternative (d) is the correct choice.



Given that 60V source is absorbing power. It means current enters in the 60V source. Let the current enter in 60V source is Le.

KCL at point 8, we get

Now, this problem is further solved by applying Hit and trail method. From the given options, we conclude that only alternative (a) satisfy the equation (i).

15. (a) From the Einstein relation

$$\begin{array}{l} \overline{D}_{0} \\ \overline{H}_{0} \end{array} = \begin{array}{l} \overline{D}_{0} \\ \overline{H}_{0} \end{array} \\ = \begin{array}{l} \overline{KT} \\ \overline{0} \\ \overline{V}_{T} = 28 mW \end{array}$$

The ratio of mobility to the diffusion constant is 1 or V11

 (d) INTR is internally generated interrupt. This interrupt is maskable and vectored. This interrupt can be delayed or rejected.

17. (c) The given network



$$\frac{V_{0}(3)}{V_{1}(z)} = \frac{1}{2 + SCR}$$
 ...0)

From given network

$$\begin{split} V_{0}(s) &= V(s) \cdot \frac{R_{c} \left[ \left| \frac{1}{C_{0}} \right]}{R + R_{c} \left[ \left| \frac{1}{C} \right]} \right]} \\ \frac{V_{0}(s)}{V_{0}(s)} &= \frac{R_{c}}{R + R_{c} + RR_{c}CS} \\ &= \frac{1}{\frac{R + R_{c}}{R_{c}} + RCS} \end{split}$$

On comparing equation (i) and (ii) we get

$$\frac{V_0(x)}{V(x)} = \frac{1}{\frac{R+R_L}{R_L} + CSR}$$

$$\frac{R+R_L}{R_L} = 2$$

$$\frac{R+R_L}{R_L} = 2R_L$$

$$\frac{R+R_L}{R_L} = R$$

Hence alternative (c) is the correct choice.

18.	(c) Given	A =	$\begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$			
	and	в =	[ <sup>p</sup> ]			
	For controllability.	C =	(B: A B)			
		AB =	[:1][;]			
		-	[]			
	Then	C =	[ 8 8 ]			
	Rank of C # Rank of A hance system is uno					
	Hence alternative (	c) is the	correct choice.			
10	(c) The river mean	ana sire	and the			

m(f) = cos (2m/m) frequency of carrier signal

 $\sin x = \sin (x - x) = -\sin (x - x)$ 

$$\begin{array}{cccc} & & f(\eta) = \frac{-i\pi h \left[ 1 - \eta \right]}{2} \\ & + \int_{-\infty}^{\infty} \left\{ \left[ 1 - \eta \right] \left[ \frac{i\pi \left[ 1 - \eta \right]}{2} \left$$

So option (d) is correct choice



28. (b) The given circuit



For t < 0 ; La. when switch was on position a for a los time it mean all the capacitors are replaced by open circuited as shown below :



From above figure V<sub>cl</sub>(0") = V<sub>cl</sub>(0") = 100V (since ourset is zero) for t > 0 : i.e. when switch is moved to continue

Equivalent capacitance is given by

$$C_{eq} = [(0.3 + 0.5) || 0.2] \mu F$$
  
 $C_{eq} = \frac{0.8 \times 0.2}{0.8 + 0.2} \mu F$ 

The circuit for this is shown below :



Hence alternative (b) is the correct of

29. (b) For maximize power to R<sub>L</sub>, R<sub>L</sub> should be equal to R<sub>B</sub> Calculation for Ra. Equivalent circuit is shown below:



V = RI+SL -Vx+41,+81+81, = 0 

 $R_m = \frac{V}{T} = 40$ 

$$-4i - 4i_1 + 4i_1 + 8i_1 + 8i_1 = 0$$
  
 $4i + 8i_1 = 0$   
 $i_1 = -\frac{1}{2}$   
w.  $V = 8i -\frac{5}{8}i = 4i$ 

Therefore

$$\begin{array}{l} \text{SD}_{n} \left( 0\right) \text{Given} & \frac{d_{n}^{2}}{dt} + \frac{V_{n}}{L^{2}} \left( 1 + B \phi^{\frac{1}{2}} \right) \right) \right) \right) \\ \text{II} \left( 1 \phi^{\frac{1}{2}} \left( 1 + B \phi^{\frac{1}{2}} \left( 1 + B \phi^{\frac{1}{2}} \left( 1 + B \phi^{\frac{1}{2}} \right) \right) \right) \right) \right) \\ \text{II} \left( 1 \phi^{\frac{1}{2}} \left( 1 + B \phi^{\frac{1}{2}} \right) \right) \right) \text{ of } + C \\ \text{II} \left( 1 \phi^{\frac{1}{2}} \left( 1 + B \phi^{\frac{1}{2}} \right) \right) \right) \\ \text{II} \left( 1 + B \phi^{\frac{1}{2}} \right) = 0 \\ \text{II} \left( 1 + B \phi^{\frac{1}{2}} \right) \right) \text{ of } + C \\ \text{II} \left( 1 + B \phi^{\frac{1}{2}} \right) \right) \\ \text{II} \left( 1 + B \phi^{\frac{1}{2}} \right) = 0 \\ \text{II} \left( 1 + B \phi^$$

$$\begin{split} & V_{00} \sum_{i=1}^{N-1} \cdots \sum_{i=1}^{N-1} \sum$$

31. (d) The given circuit



The given diade is ideal. When V<sub>i</sub> is positive the diade gets reverse biased



Hare, V = 1V

When V, is negative the diode becomes short circuited



Hare, V = - V<sub>(max)</sub>

Hance alternative (d) is the correct choice.

32. (d) In an n-channel MOSFET operating in the active region. The inversion charge decreases from source to drain as shown below :



When a voltage  $V_{ab}$  (i.e. voltage between drain and source) is applied between source and drain, with  $V_{ab} = V_b$  ( $V_{ab} = v$  voltage between gate and source and  $V_b$ is the threshold voltage) the horizontal and vertical commonents of the electrical field due to the source-drain voltage and gate-to substate voltage intract, counting conductor to occur along the channel. The horizontal component of alactic find is associated with drain-location strategies ( $\nu_{e}$ ,  $\nu_{e}$ ) to its responsible for an excepting the decrars in the channel front the source bowerds data. As the voltage from source to drain increases it means the exception of the intervent the channel potential increases from source to drain.

Hence alternative (d) is the correct choice

33. (a) The given circuit



In as table multivibrator, the value of peak voltage say,  $V_{ee}$  is fixed i.e. not changed by changing the value of  $N_{ee}$  and C but time going +  $V_{eae}$  and -  $V_{eae}$  is charged. It means frequency changes. Hence alternative (a) is the correct choice.

34. (d) The given circuit



Applying KVL in loop 1, we get  $V_0 = 0.6 + 1.4 \times 1$  $V_0 = 2V$ 

and negative feedback

35. (b) The given circuit



From given circuit, first off all we will draw thevenin equivalent circuit.

$$V_{\rm th} = V_{\rm OC} \times \frac{R_2}{R_1 + R_2}$$



The theyenin equivalent of the given circuit is shown below.



Hole,  $V_0 = -1501 \times 300$ and  $1 = \frac{V_1}{2}$ 

$$V_0 = -150 \times \frac{360}{3502} \times V_1$$
  
= -150 V.

- 150 (A cos 201 + 8 sin 10<sup>8</sup>/)
 Now the triparameter model of the thevenin equivalences before

Honce alternative (b) is the correct choice. 36. (d) The given logic equation

$$\begin{bmatrix} x + 2 \left\{ \bar{y} + \left( \bar{z} + \bar{y} \right) \right\} \left\{ \left\{ \bar{x} + \bar{z} \left( x + \gamma \right) \right\} = 3 \\ y_{11} \quad \text{if } y_{11} - 1, \text{ fran} \\ \left[ 1 + 2 \left\{ \bar{y} + \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ \bar{y} + \bar{z} \left\{ 1 + \gamma \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{y} - \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 - 2 \left\{ \bar{z} + \left( \bar{z} + \bar{y} \right) \right\} \right\} = 3 \\ \left[ 1 + 2 \left\{ \bar{z} + \left( \bar{z} + \bar{y} \right) \right\} \right] \left\{ 1 + 2 \left\{ \bar{z} + \left( \bar{z} + \bar{y} \right\} \right\} \right\} = 3 \\ \left[ 1 + 2 \left\{ 1 + 2 \left\{ \bar{z} + \left( \bar{z} + \bar{y} \right\} \right\} \right\} \right] \left\{ 1 + 2 \left\{ 1 + 2 \left\{ 1 + 2 \left\{ \bar{z} + \left( \bar{z} + \bar{y} \right\} \right\} \right\} \right\}$$

or 
$$\left[\overline{Z} + o\{\overline{Y} + \overline{Z}\}\right] = 1$$
  
 $\overline{Z} = 1$ 

37. (c) Let the inputs are A and B, and Y is the output.



From above figure

## Y = AS+AB

or Y = AB + AND gale (When S = 0

Thus, we need one 2 x 1 Maximum to realize 2-input AND gate

· For 2-input EX-OR gate,



From above figure

$$Y = A \oplus B$$

Hence alternative (c) is the correct choice. 38. (c) The piven figure :



 For NAND gate latch Initially P1, P3 inputs are 0, 1

$$\begin{array}{c} Q_1 = \overline{P_1 Q_2} \\ \text{and} \quad Q_2 = \overline{P_2 Q_1} \\ \text{gives} \quad Q_1 = 1 \\ \text{and} \quad Q_2 = 0 \end{array}$$

After few seconds P<sub>5</sub>, P<sub>2</sub> inputs becomes 1,

and 
$$Q_2 = P_2Q_1$$
  
gives  $Q_1 = 1$   
and  $Q_2 = 0$ 

· For NOR gate latch

Initially P1, P2 inputs are 0, 1

$$\begin{array}{rl} Q_1 &=& \sqrt{P_1+Q_2}\\ \text{ad} & Q_2 &=& \sqrt{P_2+Q_1}\\ \text{ves} & Q_1 &=& 1 \end{array}$$

od 0. = 0

After few seconds P1, P2 inputs becomes 1, 1

$$Q_1 = \sqrt{P_5 + Q_2}$$

and  $Q_2 = \sqrt{P_2 + Q_1}$ gives  $Q_1 = 0$ and  $Q_2 = 0$  (i.e. indeterminate condition) Hence alternative (c) is the correct choice.

99. (a)



Hence system is non causal For Bounded Input

$$y\left( \mathfrak{h} ~\leq~ \left| \int_{-\infty}^{\mathfrak{h}} \times \left( t - \tau \right) h(2\tau) ~d\tau \right|$$

or 
$$\lambda(i) \neq |x(i-i)| \sum_{i=0}^{n} |\psi(5i)| \psi_i < =$$

Hence system is bounded output

$$\begin{split} H(0)_{n+1} &= -\frac{e^{2} + 1}{12 + 2e^{-1}} \\ & a & \\ H(0-1) &= -\frac{1 + 2e^{-1}}{1 + 2e^{-1}} \\ H(1-1) &= -\frac{1 + 2e^{-$$

(6. (b) (Also product of eigen values = det A.)

42	1	4	9	16	25
8	1	2	3	4	5
P(X = k)	0-1	0.2	0-4	0.2	0.1
PR	0-1	0.4	12	0.8	0.5

 $\begin{array}{l} \mu_{1} + \lambda \tan contractor (y \ math m - 2 \ math m -$ 

 $\eta = \left[\frac{Pt - P_c}{P_c}\right] \times 100$ 

 $m = \sqrt{m_1^2 + m_2^2} = \sqrt{\frac{1}{4} + \frac{1}{4}} = \frac{1}{\sqrt{2}}$  $P_{\varepsilon} = P_{\varepsilon} \left[ 1 + \frac{1}{4} \right] = P_{\varepsilon} \left[ \frac{5}{4} \right]$ 4Pt = 5F  $\Rightarrow 4[P_t - P_n] = P_n$  $\eta = \frac{P_{el4} \times 100}{50}$ n = 1 × 100 = 20% = B. casacity C. We have, C = B h [1+8] C1 = Bh 1+5 C. - 8 .... and if & doubled C2 = Bh 1+2% C. . 8h2 C. - Bh2+Bh C. . . .  $\vec{B} = B_0 \left[ \frac{x}{x^2 + x^2} - \hat{y} - \frac{y}{x^2 + x^2} \hat{x} \right]$ We have, H =  $\nabla \times \mathbf{H} = \begin{bmatrix} \mathbf{j} & \mathbf{j} & \mathbf{k} \\ \frac{\partial}{\partial x} & \frac{\partial}{\partial y} & \frac{\partial}{\partial z} \\ -\frac{B_0}{\mu_0} \cdot \frac{\mathbf{y}}{\mathbf{x}^2 + \mathbf{y}^2} & \frac{B_0}{\mu_0} \frac{\mathbf{x}}{\mathbf{x}^2 + \mathbf{y}^2} & \mathbf{0} \end{bmatrix}$  $\nabla \times H = i \left[ \frac{\partial}{\partial x} \times 0 - \frac{B_0}{H_0} \frac{\partial}{\partial z} \frac{x}{z^2 + y^2} \right] + i \left[ 0 + \frac{B_0}{H_0} \frac{\partial}{\partial z} \frac{y}{z^2 + y^2} \right]$  $-k \frac{B_0}{\mu} \left[ \frac{\partial}{\partial x} \cdot \frac{x}{x^2 + y^2} + \frac{\partial}{\partial y} \frac{y}{x^2 + y^2} \right]$  $v \times H = -\frac{B_0}{u_0} \cdot \frac{2}{r} \cdot \left(\frac{2}{r^2 + r^2}\right) r \neq 0$ → Z<sub>0</sub> = 50Ω

We have  $Z_i = Z_0 \begin{bmatrix} Z_i + Z_0 / \tan \beta_i \\ Z_0 + Z_i / \tan \beta_i \end{bmatrix}$ Branch (1)  $\beta_L = \frac{2\pi}{\lambda} \cdot \frac{\lambda}{4}$  $Z_{i} = Z_{0} \begin{bmatrix} Z_{L} + Z_{0} j \tan \frac{\pi}{2} \\ Z_{0} + Z_{L} j \tan \frac{\pi}{2} \end{bmatrix}$  $z_i = \frac{z_0 \cdot z_0}{z_i}$ \_ 100 × 100 = 200 Branch (2) Zi = 201 Then for Branch (3) Z = 50 × 50  $= 25\Omega \cdot \frac{200 \times 200}{400} = 100\Omega$ Overall,  $Z_1 = \frac{Z_0 Z_0}{Z_1} = \frac{50 \times 50}{100} = 25 \Omega$ 51, (b) x. = 0-1 pm n = 14 x 10<sup>10</sup> cm<sup>-3</sup> Vr = 26 ms 1. = 12 ra = 8.85 x 10" 14 F- (ce)"  $V_0 = \frac{KT}{a} \ln \left[ \frac{N_s N d}{n^2} \right]$ AT = VT We know the x. N. = x.N.  $N_{\mu} = \sum_{j=1}^{N_{\mu}} N_{j}$  $=\frac{0.1}{1.7}\times 10^{17}=10^{15}$  $V_0 = 28 \times 10^{-3} \ln \left[ \frac{10^{16} \times 10^{17}}{(1.4 \times 10^{10})^2} \right]$ = 0.76 Vet 1-6 × 10<sup>-10</sup> 8-85 × 10<sup>-14</sup> × 12 × 1 × 10<sup>17</sup> × 0.1 × 10-0 × 100 = 0-15 MV cm<sup>-1</sup>

Given a = 0-5 GM = 2000 = 20 log 10 0.5 0.0 PM = 180-24 2.6 = 90 from figure DM = 1801 - 801 - 801 55. (c)  $\left(\frac{S}{N}\right)_{a}$  = 43-5 dB  $\left(\frac{8}{N}\right)_{68} \approx 18 + 6n = 43.5$  $\Delta = \frac{2m_{F}}{2T} = \frac{2 \times 6}{2T} = 0.07$ Option (c) is correct A1 = 0.05 V for positive value A= = 0-1V for negative value  $L_1 = \frac{2m_p}{1} = \frac{5}{0.00} = 100$  $L_2 = \frac{2m_p}{\Delta_2} = \frac{5}{0.1} = 50$ L = L1 + L2 = 150 = 2<sup>ch</sup> INR) # 1-72+6-02 n = 172+602×7 a 43-86 dB 57. (c) The olven circuit 3 Volt In c-MOS circuit z-MOS is connected to supply voltage while n-MOS is connected to ground voltage. Vr = 1V for both type MOSFET niven For n-MOS Vr. = 1V and Vo is small increase in 1 volt Hence Ver - Vr = 0 or slightly greates than zero. Then n-MOS is in glode

For p-MOS Vga = 3-5=-2 volt

which is more negative to  $V_{TP}.$  Hence it is in saturation region,

Hence allemative (c) is the correct choice. (d) Given V<sub>in</sub> = 1.6 volt

3 Vo-

From transistor (1)

 $V_{1n} = 1V$  $V_{00} - V_{2n} = 1.5 - 1 = 0.5V$ 

Hence the transistor (1) is in seturation.

Then, 
$$\begin{split} I_D &= \mu \ C_{ex} \frac{W}{2L} [V_{D0} - V_{D0}]^2 \\ \mu \ C_{ex} \frac{W}{L} &= 1 \ mA \\ I_D &= \frac{1}{2} [1 \cdot S - 1]^2 = \frac{1}{8} mA \end{split}$$

and from transistor (2)

$$\begin{split} I_{D} &= \mu \, \mathbb{C}_{BV} \frac{W}{L} \left[ (V_{DS} - V_{TS}) \, V_{DS} - \frac{\sqrt{2} g_{SS}}{2} \right] \\ &\frac{1}{8} &= \frac{1}{2} \left[ (2 - 1) \, V_{DS} - \frac{\sqrt{2} g_{SS}}{2} \right] \\ &\frac{1}{6} &= \sqrt{2} g_{DS} - 4 V_{DS} + 1 \end{split}$$

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Hence  $V_{D3} = 1 - \frac{\sqrt{3}}{2}$  $V_D = 5 - V_{D3}$ 

 $V_0 = 5 - 1 + \frac{\sqrt{3}}{2} = 4 + \frac{\sqrt{3}}{2}$ 

9. (b) The given 7 segment display



Here the inputs are P<sub>1</sub> and P<sub>2</sub> and outputs are a, b, c, d, e, f, g, in order to solve this problem we have to calculate g in terms of P<sub>1</sub> and P<sub>2</sub>.

Inputs		culputa						
Ρ,	P2	a	b	0	đ		1	5
0	0	1	1	1	1	1	1	0
0	1	1	0	1	1	0	1	11
1	0	1	0	1	1	0	1	
1	1	1	0	0	4	1	11	14

· K-map for a

K-map for b





 $\begin{aligned} \sigma &= \overline{P_1 P_2} * \overline{P_1 P_2} \\ \text{ince,} & g &= P_1 + P_2, \end{aligned}$ 

(since A + AB = A + B so, P<sub>2</sub> + P<sub>2</sub>P<sub>2</sub> = P<sub>2</sub> + P<sub>1</sub>)

= 
$$P_1 + \vec{P}_1 + \vec{P}_2$$
  
=  $1 + \vec{P}_2$  (since  $P_1 + \vec{P}_1 = 1$ )  
=  $1 = d$  (since  $1 + \vec{P}_2 = 1$ )

Therefore, alternative (b) is the correct choice.



Thus minimum number of 3-NOT gates and 4-OR gates are required to design the logic of the driver. Hence alternative (a) is the correct choice.